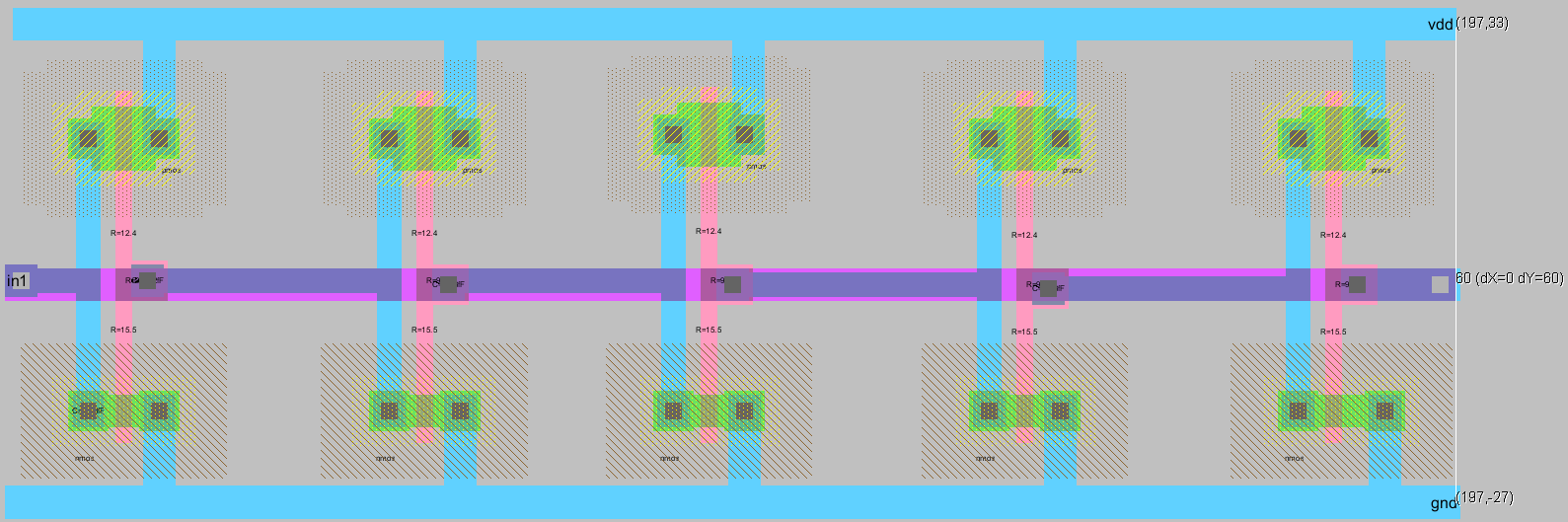
**3a - Five Stage Ring Oscillator**

Layout

Layout for Five Stage Ring Oscillator with height of the cell as 660nm:



DRC:

Running DRC with area bit on, extension bit on, Mosis bit

Checking again hierarchy .... (0.0 secs)

Found 18 networks

0 errors and 0 warnings found (took 0.001 secs)

LVS:

Hierarchical NCC every cell in the design: cell 'FiveStageRingOsc{sch}' cell 'Demo\_\_FiveStageRingOsc{lay}'

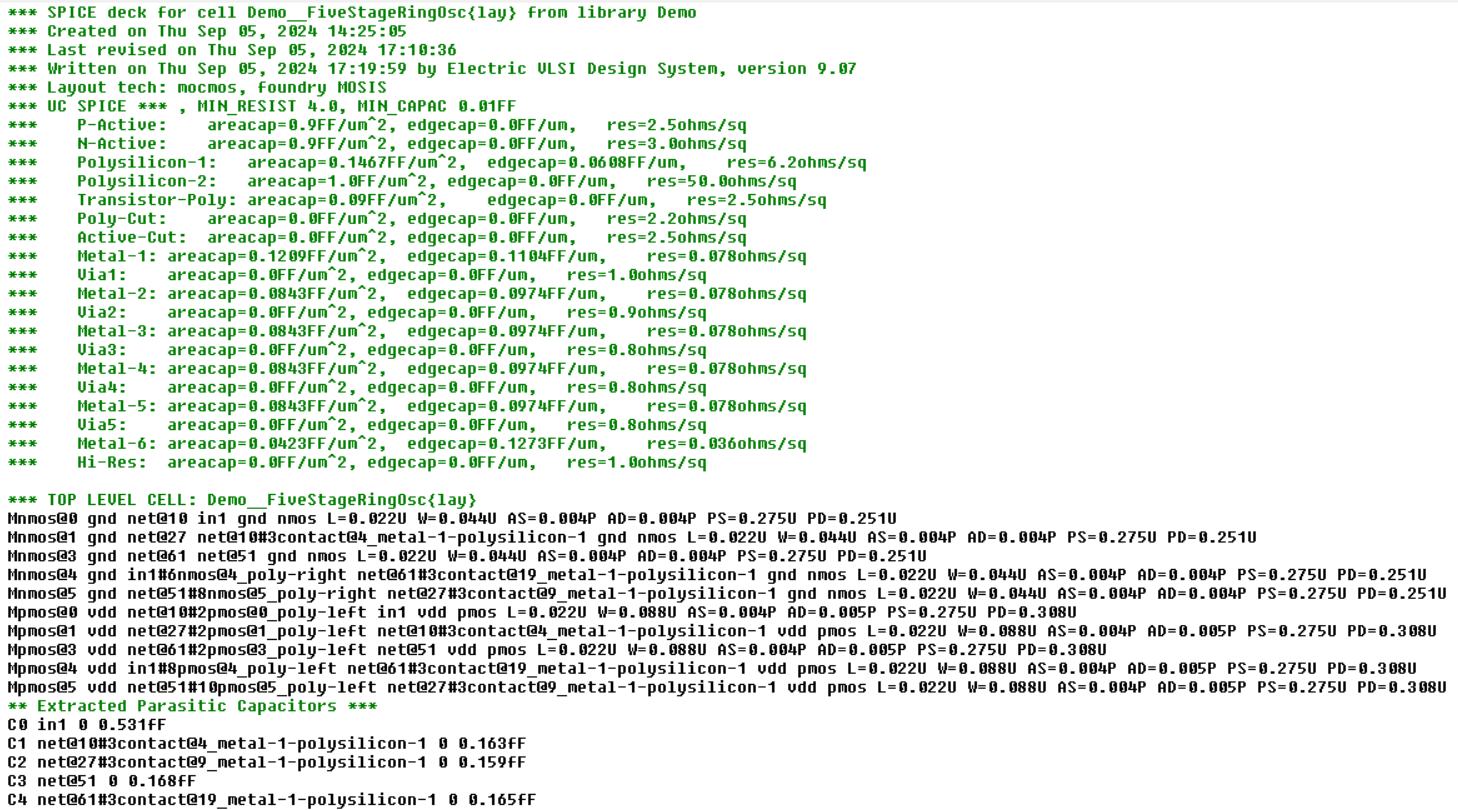
Comparing: Demo:FiveStageRingOsc{sch} with: Demo:Demo\_\_FiveStageRingOsc{lay}

exports match, topologies match, sizes match in 0.003 seconds.

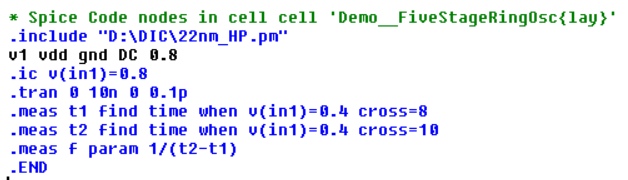
Summary for all cells: exports match, topologies match, sizes match

NCC command completed in: 0.003 seconds.

Spice Code:

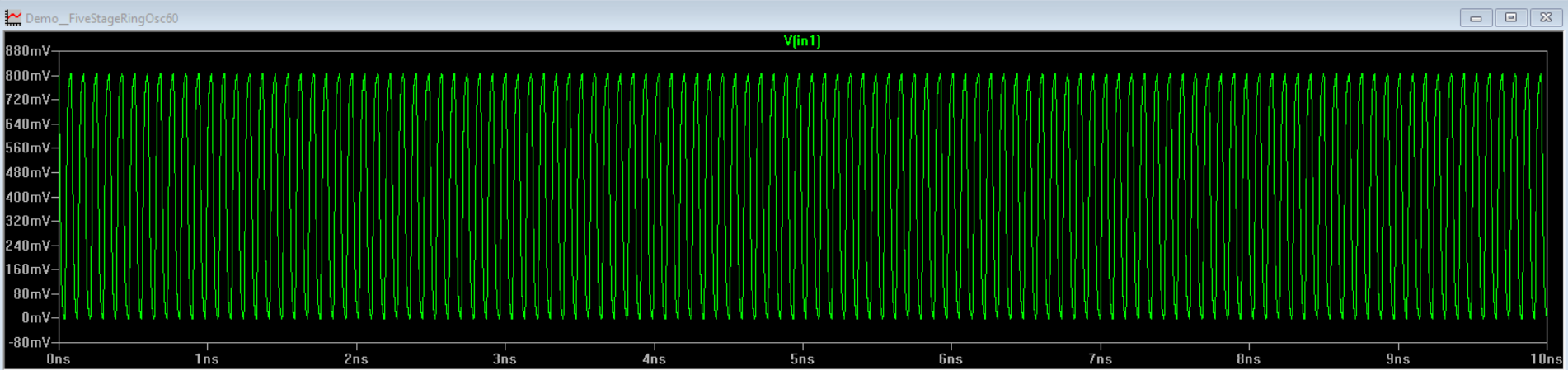






Measurement:

ID vs t



Error Log file of Simulation:

